

WHAT IS CLAIMED IS:

1. A method of creating an image, the method comprising:
operating a display to create a sequence of bit display periods, said bit display periods comprising:
at least one conflict bit period skewed with respect to other said bit display periods; and
at least two compensating bit periods having a bit period such that an error created by said skewing occurs during said compensating bits.
2. The method of Claim 1 in which said bit period of a first of said at least two compensating bit periods is shortened and said bit period of a second of said at least two compensating bit periods is lengthened.
3. The method of Claim 1 in which said bit period of a first of said at least two compensating bit periods is lengthened and said bit period of a second of said at least two compensating bit periods is shortened.
4. The method of Claim 1 in which a first and a second of said at least two compensating bit periods are segments of the same image bit.
5. The method of Claim 1 in which said bit periods of a first and a second of said at least two compensating bit periods are temporally adjacent said at least one conflict bit period.
6. The method of Claim 1 in which said bit periods of a first and a second of said at least two compensating bit periods are temporally adjacent a combination of said at least one conflict bit period and at least one other bit period.
7. The method of Claim 1 in which said bit period of a first of said at least two compensating bit periods occurs prior to said at least one conflict bit period and a second

of said at least two compensating bit periods occurs following said at least one conflict bit period.

8. The method of Claim 7 in which said first compensating bit period corresponds to a first image bit and a second compensating bit period corresponds to a second image bit.
9. The method of Claim 8 comprising displaying another bit segment corresponding to said first image bit and another bit segment corresponding to said second image bit in said sequence of bit display periods such that said two another bit segments compensate for said error created by said skewing.
10. The method of Claim 9 displaying at least one other image bit between said another bit segment corresponding to said first image bit and said another bit corresponding to said second image bit.
11. A display comprising:
 - an image data source providing a plurality of image data bits; and
 - a display device comprising at least one display element operable to form an image pixel corresponding to a plurality of image data bits over a sequence of bit display periods, said bit display periods comprising:
 - at least one conflict bit period skewed with respect to other said bit display periods; and
 - at least two compensating bit periods having a bit period such that an error created by said skewing occurs during said compensating bits.
12. The display of Claim 11 in which said bit period of a first of said at least two compensating bit periods is shortened and said bit period of a second of said at least two compensating bit periods is lengthened.

13. The display of Claim 11 in which said bit period of a first of said at least two compensating bit periods is lengthened and said bit period of a second of said at least two compensating bit periods is shortened.
14. The display of Claim 11 in which a first and a second of said at least two compensating bit periods are segments of the same image bit.
15. The display of Claim 11 in which said bit periods of a first and a second of said at least two compensating bit periods are temporally adjacent said at least one conflict bit period.
16. The display of Claim 11 in which said bit periods of a first and a second of said at least two compensating bit periods are temporally adjacent a combination of said at least one conflict bit period and at least one other bit period.
17. The display of Claim 11 in which said bit period of a first of said at least two compensating bit periods occurs prior to said at least one conflict bit period and a second of said at least two compensating bit periods occurs following said at least one conflict bit period.
18. The display of Claim 17 in which said first compensating bit period corresponds to a first image bit and a second compensating bit period corresponds to a second image bit.
19. The display of Claim 18 comprising displaying another bit segment corresponding to said first image bit and another bit segment corresponding to said second image bit in said sequence of bit display periods such that said two another bit segments compensate for said error created by said skewing.
20. The display of Claim 19 displaying at least one other image bit between said another bit segment corresponding to said first image bit and said another bit corresponding to said second image bit.